

Fig. 1

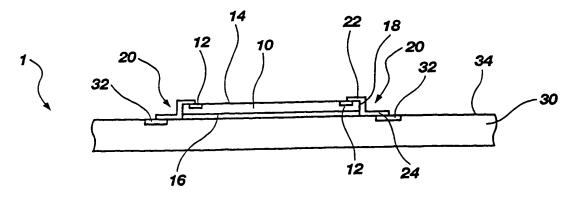
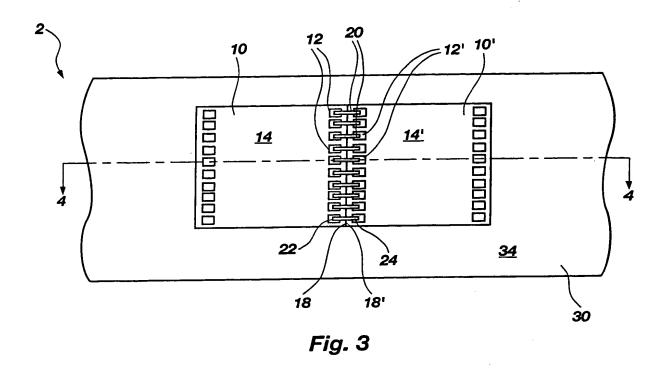
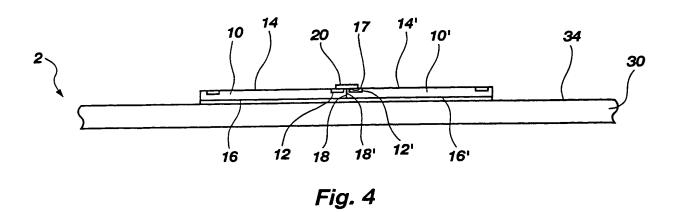


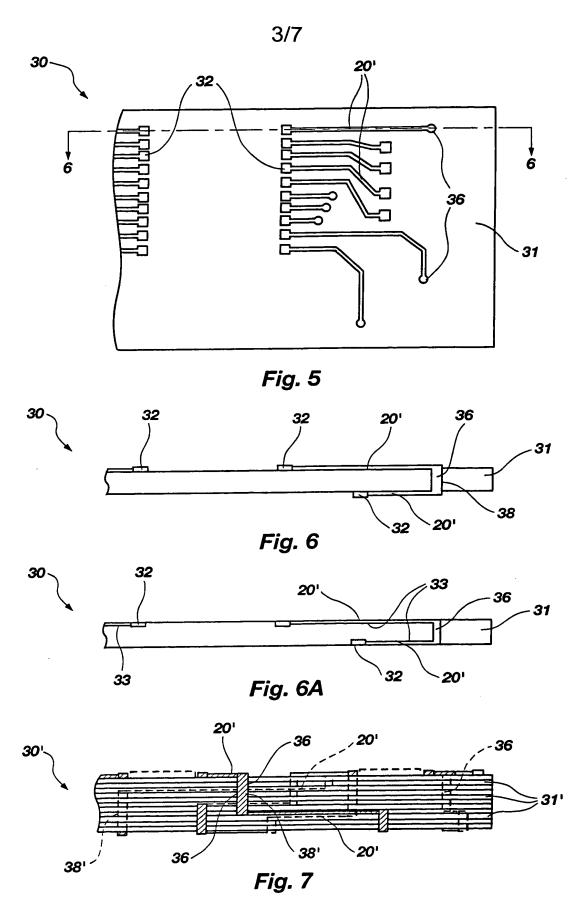
Fig. 2

TITLE: STEREOLITHOGRAPHIC METHODS FOR SECURING CONDUCTIVE ELEMENTS TO CONTACTS OF SEMICONDUCTOR DEVICE COMPONENTS Inventor: Vermon M. Williams Docket No.: 2269-4208.8US





TITLE: STEREOLITHOGRAPHIC METHODS FOR SECURING CONDUCTIVE ELEMENTS TO CONTACTS OF SEMICONDUCTOR DEVICE COMPONENTS Inventor: Vernon M. Williams Docket No.: 2269-4208.8US



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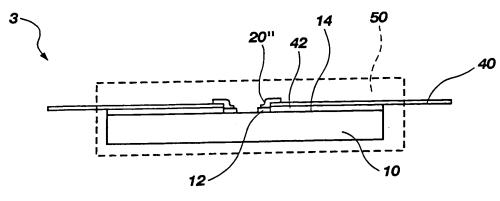
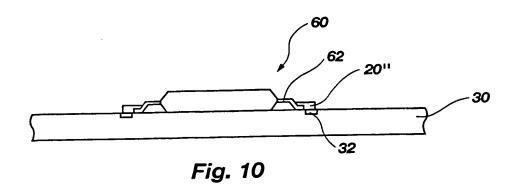
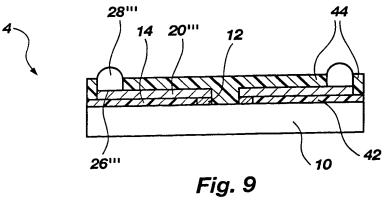


Fig. 8





DEVICE COMPONENTS Inventor: Vernon M. Williams Docket No.: 2269-4208.8US

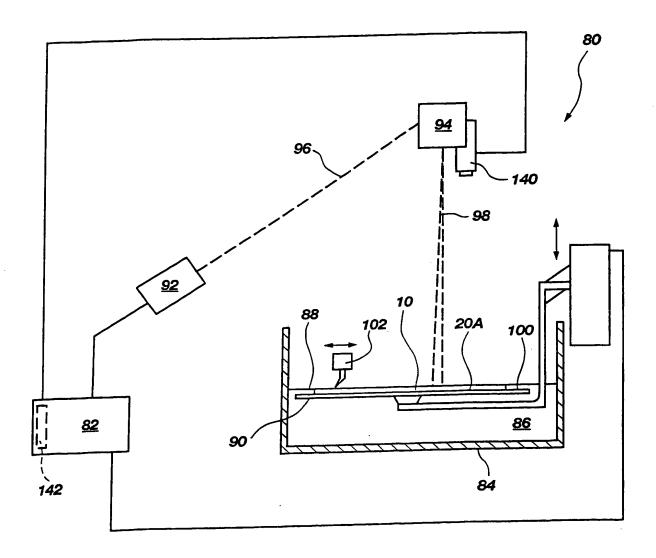


Fig. 11

DEVICE COMPONENTS Inventor: Vernon M. Williams Docket No.: 2269-4208.8US

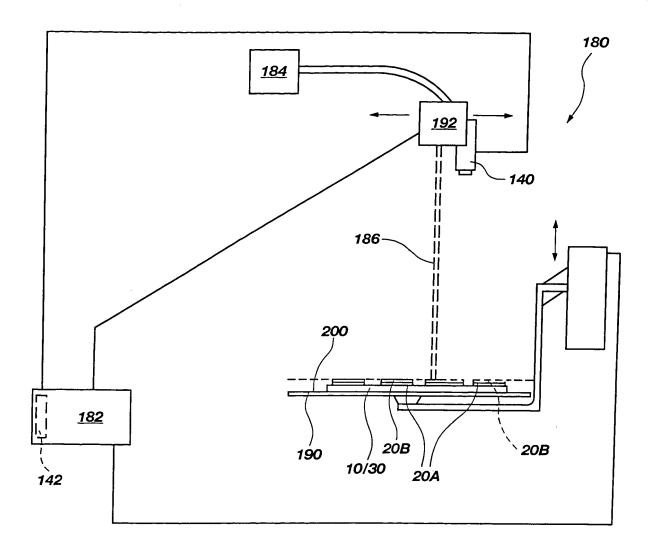


Fig. 12

TITLE: STEREOLITHOGRAPHIC METHODS FOR SECURING CONDUCTIVE ELEMENTS TO CONTACTS OF SEMICONDUCTOR DEVICE COMPONENTS Inventor: Vernon M. Williams Docket No.: 2269-4208.8US

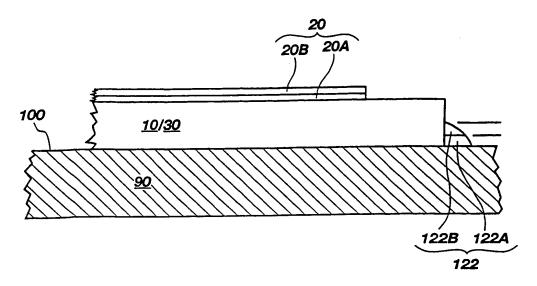


Fig. 13